

L Number	Hits	Search Text	DB	Time stamp
13	0	((set near2 function) with (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))	USPAT; EPO; JPO; DERWENT	2003/10/15 09:11
14	6	((set near2 function) same (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))	USPAT; EPO; JPO; DERWENT	2003/10/15 09:10
15	0	((set near2 function) with (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and ((integrated near1 circuit) or IC or VLSI or LSI) and (design\$3 or simulat\$3 or model\$4)	USPAT; EPO; JPO; DERWENT	2003/10/15 08:58
16	0	((set near2 functions) with (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and ((integrated near1 circuit) or IC or VLSI or LSI) and (design\$3 or simulat\$3 or model\$4)	USPAT; EPO; JPO; DERWENT	2003/10/15 08:58
17	2	((set near2 functions) with (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network))	USPAT; EPO; JPO; DERWENT	2003/10/15 08:59
18	12	((set near2 functions) same (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network))	USPAT; EPO; JPO; DERWENT	2003/10/15 09:21
19	152907	((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4)	USPAT; EPO; JPO; DERWENT	2003/10/15 09:03
20	186448	((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/15 09:03
21	0	((set near2 function) with (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4))	USPAT; EPO; JPO; DERWENT	2003/10/15 09:04
22	6	((set near2 function) same (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4))	USPAT; EPO; JPO; DERWENT	2003/10/15 09:08
23	6455	(set near2 functions) and (((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/15 09:10
24	179	(sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network) and ((set near2 functions) and (((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/15 09:11
25	6	((set near2 functions) with (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/15 09:12
26	13	((set near2 functions) same (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/15 09:12

27	21	((set near2 functions) same (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/15 09:21
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